

1c690 U.S. PTO
05/24/00

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1c682 U.S. PTO
09/576956
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May 24, 2000

BOX PATENT APPLICATION
Assistant Commissioner for Patents
Washington, D.C. 20231

Re: Application of RAGHUNATHAN, Anand, LAKSHMINARAYANA, Ganesh, LAHIRI,
Kanishka and DEY, Sujit
A METHODOLOGY FOR THE DESIGN OF HIGH-PERFORMANCE
COMMUNICATION ARCHITECTURES FOR SYSTEM-ON-CHIPS USING
COMMUNICATION ARCHITECTURE TUNERS
Our Ref. A7680

Dear Sir:

Attached hereto is the application identified above including 52 sheets of the specification, claims, and 17 sheets of drawings. **The executed Declaration and Power of Attorney and Assignment will be submitted at a later date.**

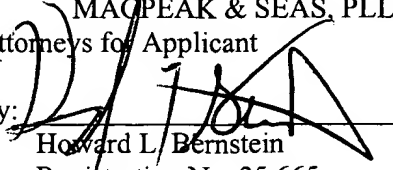
The Government filing fee is calculated as follows:

Total claims	9 - 20	=		x	\$18.00	=	\$0.00
Independent claims	1 - 3	=		x	\$78.00	=	\$0.00
Base Fee							\$690.00
TOTAL FEE							\$690.00

A check for the statutory filing fee of \$690.00 is attached. You are also directed and authorized to charge or credit any difference or overpayment to Deposit Account No. 19-4880. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 and any petitions for extension of time under 37 C.F.R. § 1.136 which may be required during the entire pendency of the application to Deposit Account No. 19-4880. A duplicate copy of this transmittal letter is attached.

This application claims priority from co-pending U.S. Provisional Patent Application No. 60/186,751 filed March 3, 2000.

Respectfully submitted,
SUGHRUE, MION, ZINN,
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By: 
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